



PEARL

Utilization of harmonics current in single phase system

Al-Bayaty, H; Ambroze, M; Ahmed, MZ

Published in:

Host publication not specified in Elements

DOI:

[10.1109/ICHQP.2016.7783468](https://doi.org/10.1109/ICHQP.2016.7783468)

Publication date:

2016

Link:

[Link to publication in PEARL](#)

Citation for published version (APA):

Al-Bayaty, H., Ambroze, M., & Ahmed, MZ. (2016). Utilization of harmonics current in single phase system. In *Host publication not specified in Elements* (Vol. 0, pp. 443-447) <https://doi.org/10.1109/ICHQP.2016.7783468>

All content in PEARL is protected by copyright law. Author manuscripts are made available in accordance with publisher policies. Wherever possible please cite the published version using the details provided on the item record or document. In the absence of an open licence (e.g. Creative Commons), permissions for further reuse of content should be sought from the publisher or author.

Utilization of Harmonics Current in Single Phase System

Hussein Al-bayaty
School of Computing, Electronics
and Mathematics
University of Plymouth, UK
Hussein.al-bayaty@plymouth.ac.uk
kably30@gmail.com

Marcel Ambroze
School of Computing, Electronics
and Mathematics
University of Plymouth, UK
M.Ambroze@plymouth.ac.uk

Mohammed Zaki Ahmed
School of Computing, Electronics
and Mathematics
University of Plymouth, UK
M.Ahmed@plymouth.ac.uk

Abstract—This paper presents a simple method to benefit of the harmonics current which is produced on the source side because of the nonlinear loads in single phase systems. The proposed circuit contributes in reduction of the total harmonic distortion (THD) and draw harmonics current via low pass filter (LPF) and create a useful power by rectifying the distorted current via full wave bridge rectifier (convert it to DC current) and reconvert it to AC sinusoidal current by using single phase inverter controlled by PWM circuit in order to feed different loads. The circuit has been designed and simulated in the MATLAB-Simulink program.

It can be concluded from the results of the simulation, that the utilized current (I_u) can be created by rectifying harmonics current I_h with simple design and low cost circuit without requiring any additional current drawn from the source and can feed RL-load.

I. KEYWORDS

Harmonics Elimination, Passive Filters, Total Harmonic Distortion (THD)

II. INTRODUCTION

The harmonic distortion is very old phenomenon though it has been discovered since 19th century. The main issue at that time was the effect of harmonics on the electric machines and the interference with telephone lines. However, the invention of power electronics devices and variable frequency drives (VFD) has created big concerns and serious problems for public utilities, distribution systems and the customers[1].

Different methods have been used to eliminate harmonics current from the power system [2] [3] [4]. Due to its simplicity of design, simplicity of control, low cost and variety of types, passive filter is one of the most popular methods. However, its depends on the principle of providing low impedance path through X_L (for low frequencies) and low impedance through X_C (for high frequencies) in order to take out the harmonic current and grounding it [5]. for huge power systems, if the total harmonic distortion (THD) and then the total harmonics current have been calculated in every subsystem, then it can be concluded that the system loses every day hundreds of harmonics current in total by grounding it.

In example, if we have a power supply with non-linear load draws fundamental current ($I_1 = 70$ A) and harmonics current ($I_h = 20$ A), so the supply has to feed total supply current (I_t

= 72.8 A) because ($I_t^2 = I_1^2 + I_h^2$) and THD = 28 %. In this case the useful current is I_1 and the lost current which going to be grounded is I_h . The idea of taking benefit of harmonics current can invest 20 A (in this case), as a free power can feed different loads by using conventional methods (rectifier and inverter or active front end).

III. HARMONICS UTILIZATIONS

Researchers tried to invest the harmonics phenomenon through using it in some applications in order to address some problems, such as:

- 1) Islanding Detection: Islanding detection has many negative effects on the power grid [6]. According to [7], Islanding can be detected by measuring the harmonics which have been generated by the over modulating inverter.
- 2) Power Exchanger in DPFC: One of the components within the flexible AC-transmission system (FACTS) family, is Distributed Power-Flow Controller (DPFC). DPFC has used third-harmonic frequency as an active power exchanger between the shunt and series converters instead of the common DC link [8], [9].
- 3) Identifying of Problems: Harmonics measurement and an on-line analysis of the system's harmonics, may be necessary to properly identify the cause of some problems and recommend solutions. This usually involves a harmonic analysis of the current or voltage waveforms observed in the electrical system [10].
- 4) Improving Inverter Utilization: Harmonic injection enables an improvement in the DC bus utilization for the multiphase voltage source inverter (VSIs) without moving into the over-modulation region, while still providing sinusoidal output phase voltages [11] [12].
- 5) Losses Reduction Method: The authors in [13] propose to add a third harmonic to the PWM reference signal to alter the harmonic spectrum of sinusoidal PWM inverters and thus reduce motor losses.
- 6) Reuse the harmonic current in three phase system: The authors in [14] [15] presented a new idea to reuse the harmonics via proposing a new topology of three phase passive filter [14] and a new topology of three phase

hybrid active power filter (HAPF) [15]. However, these topologies are still complex in design as represent three phase system.

This article presents the idea of utilizing harmonics current and reuse it to feed different loads via using passive filter and active front end with simpler design in single phase system.

Firstly, the methodology has been presented and a schematic diagram has been designed, secondly, the circuit has been investigated and tested via Matlab-Simulink program, finally, the results have been assessed through comparing the results in three cases.

IV. SYSTEM DESCRIPTION (METHODOLOGY)

A simplified schematic of the circuit is shown in the figure (1), the circuit consists of a single phase 220 V AC voltage source connected in series with low pass side of the passive (LC) filter (100 mH inductor) and in parallel (but not grounded) with high pass side (0.1 mF capacitor). This capacitor will take out all harmonics current (over 50 Hz) passes it into the new proposed utilization circuit (consists of full wave bridge rectifier and 4-pulses inverter). The output current is sinusoidal current and can be used to feed different electrical loads.

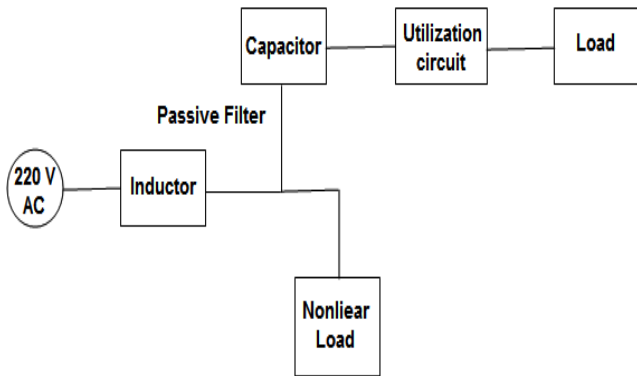


Fig. 1. Schematic diagram

V. CASES TO STUDY

The following three cases will show the differences in results between a circuit with nonlinear load and the same circuit with conventional LPF and the same circuit with different topology of LPF with the active front end. The results and figures have been collected and classified to make a comparison between these cases in the following items :

- 1) First case: AC power supply 220 v with nonlinear load (a single diode with RL-load) produces $THD_I = 43.35\%$, without use of any filter as shown below in figure 2: THD_I and THD_V can be changed with size and type of the nonlinear load.
- 2) Second case: In addition to the same circuit (in figure 2) a conventional low pass filter (LPF) will be connected in series with L and in parallel with C and a small resistor in order to measure the harmonics current as it is shown in the figure 3.

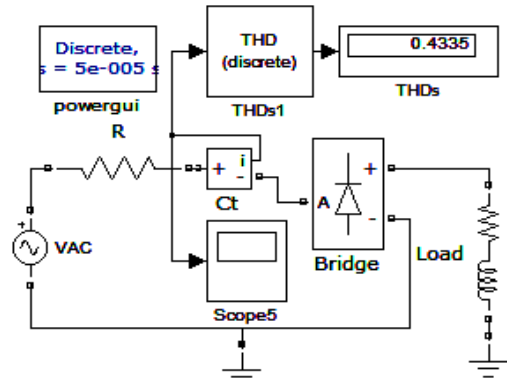


Fig. 2. First case circuit

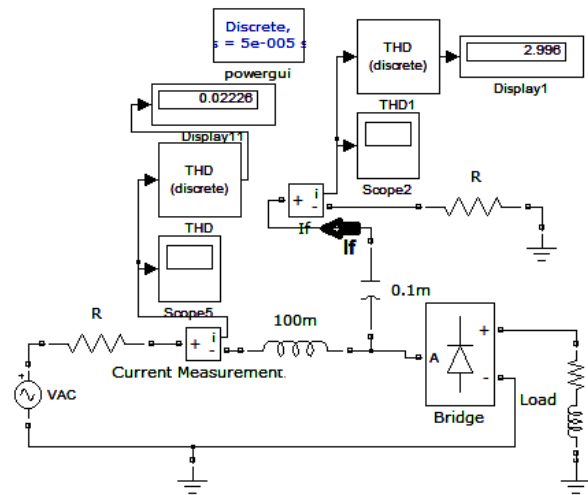


Fig. 3. Second case circuit

THD_I is decreased into 2.2%, the source current I_s is 7.2 A, and filter current I_f (in the capacitor branch) is 2.1 A (distorted current) $THD_I = 300\%$. Figure 4 shows the filter current waveform or the extracted harmonics current in the case of using LPF.

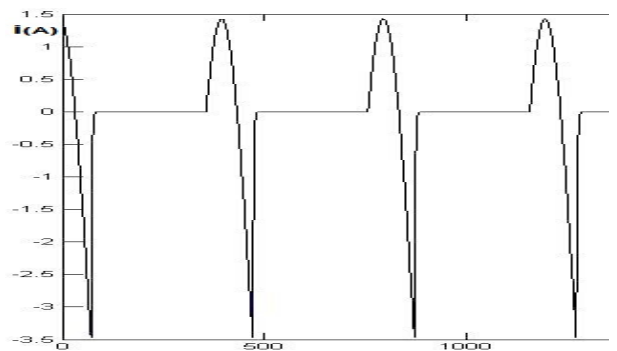


Fig. 4. Filter current in the second case

- 3) Third case: The same circuit (as fig. 3) has been connected in addition to the utilization circuit in fig. 5.

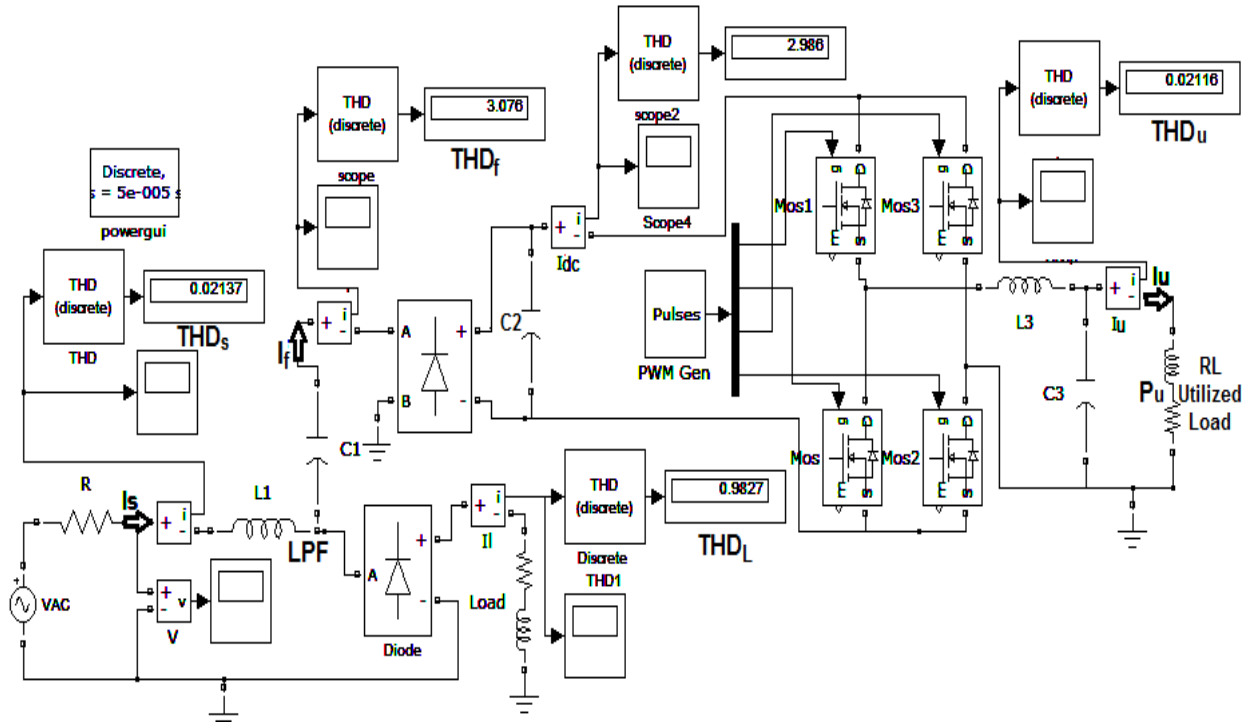


Fig. 5. Third case circuit

The utilization circuit consists of a bridge rectifier with four pulses single phase inverter controlled by PWM circuit, the inverter is connected to RL-load as a utilized load. The capacitor C_2 after the bridge rectifier has been used as a dc voltage storage, and its value can be chosen as 1.1 mF or higher depending on the load current, voltage and on the required value of THD_u . The source current I_s in figure 6 has $THD_s = 2.2\%$. The output utilized current waveform $I_u = 1.4$ A is sinusoidal as shown in figure 6, with very low $THD_I = 1.7\%$ and the source current I_s still 7.2 A without any extra current drawing.

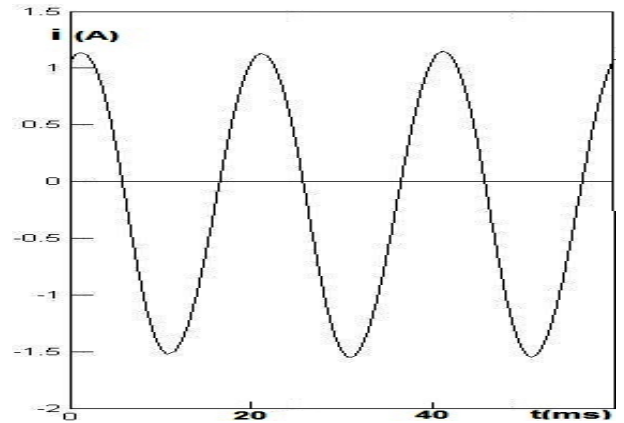


Fig. 7. Utilization current I_u waveform

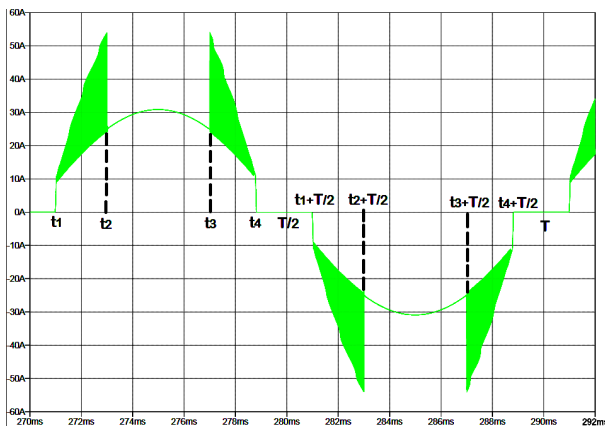


Fig. 6. Source current in the 3rd case

VI. SIMULATION RESULTS ASSESSMENT

This study required three cases to study and compare results of each case. The first case shows a single phase voltage source in series with a single diode and inductive load as a nonlinear load. In this case $THD_I = 43.35\%$ without using any filter.

The second case is the same circuit adding a conventional LPF with $L = 100$ mH, $C = 0.1$ mF and tuning frequency = 50 Hz. THD_I of the source has decreased to 2.2% and the source current (I_s) in this case is 7.2 A, while the harmonic current or filter current (I_f) at the high frequency (capacitor) branch is highly distorted as shown in figure 4.

The third case shows the full circuit adding a utilization circuit consisting of full wave bridge rectifier and single phase

inverter controlled by PWM circuit in series with the utilized load (RL-load). I_s and I_h remained at the same value, however a new current will appear on the output of inverter, which is the utilized current $I_u = 1.4$ A that feeds RL-load. This I_u current has been created from the rectification of the harmonics current into sinusoidal current with just $THD_I = 1.7\%$ at the load side.

The table below displays the values of I_s , I_u , distortion of the source (THD_s) & distortion of the filter (THD_f) which change against specific values of L and C choosing in a way that investigate the 50 Hz tuning frequency for LPF but in different filter impedances.

	L(m)	C(m)	I_s	I_u	THD_s	THD_f
Case 1	0	0	110	0	43.3	0
Case 2	100	0.1	7.2	0	2.2	289
Case 3	100	0.1	7.2	1.4	1.7	305
Case 3	50	0.2	21	4	3.2	246
Case 3	18	0.56	38	13.7	5.6	180
Case 3	10	1	61	22	8.8	164
Case 3	1	10	123	43	27	133
Case 3	0.5	20	149	67	51	126

The first row shows the big value of ($THD_s = 43.35\%$) in the first case, because the use of nonlinear load without adding any filter.

The second row shows that the value of I_s has decreased into 7.2 AC ampere, and THD_s decreased to 2.2% with very high $THD_f = 289\%$ on the capacitor's branch of LPF because the capacitor offers low impedance X_C in high frequencies and isolate the harmonics current from the source. These values reflect the effectiveness of the LPF to draw the harmonics current.

The 3rd, 4th, 5th, 6th, 7th & 8th rows show the values of I_s , I_u , THD_s & THD_f in the third case when using a utilizing circuit in six different values of L and C which tunes the LPF's frequency at the 50 Hz fundamental frequency.

Figure 8, presents the values of L & C against filter impedance (Z_f) in the 3rd case. It shows that the filter impedance Z_f directly proportion with inductance value and inversely with capacitance value of LPF.

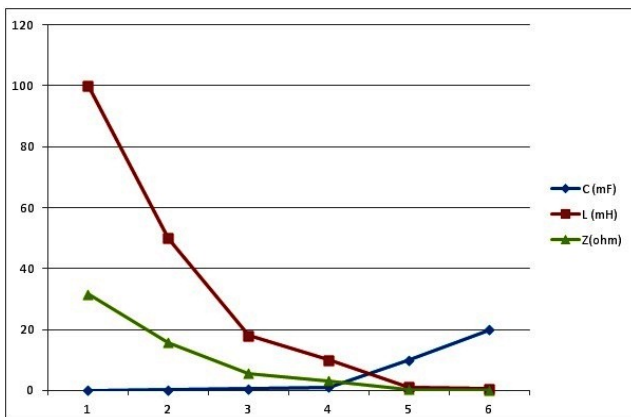


Fig. 8. The values of L & C against filter impedance

Figure 9, shows a comparison between THD_s & THD_f in six different values of filter impedance of the 3rd case. The values of (THD_s) is increasing and the values of (THD_f) is decreasing. Therefore, it can be concluded that the LPF decreased its ability to draw harmonics when (C) is increasing and (L) and (Z_f) is decreasing.

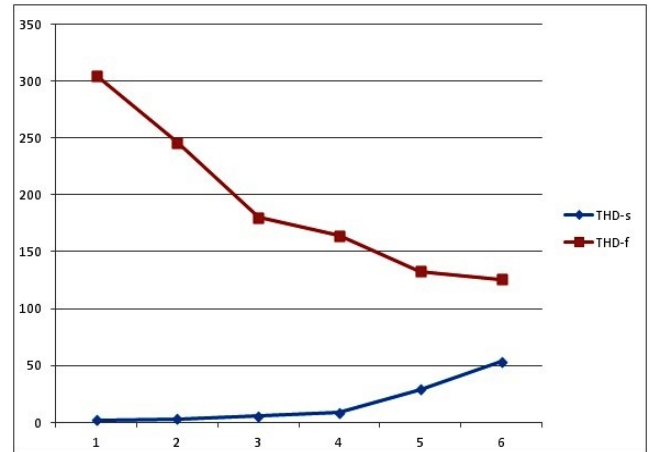


Fig. 9. Comparison between THD_s & THD_f in six values of L & C

On the other hand, figure 10 shows a comparison between I_s and I_u values in the 3rd case and proves that I_s and I_u are directly proportional with C Value and inversely with L and Z_f values. This phenomenon means that, the low impedance LPF will cost the source additional current but in the same time, will produce additional I_u after utilization circuit.

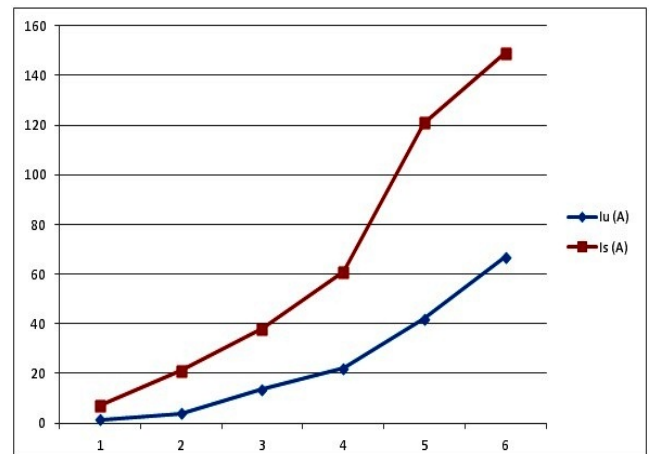


Fig. 10. Comparison between I_s & I_u in 6 values of L & C

The chosen value of impedance filter $Z_f = 31.6 \Omega$ in the third row, when $L = 100$ mH and $C = 0.1$ mF is the optimum value for this circuit, because it does not require additional source current and can get a sinusoidal $I_u = 1.4$ A on the output.

VII. CONCLUSION

This article presented a simple design of LPF using a simple method in order to utilize harmonics current in single phase systems by filtering out the harmonics current I_h via LPF and passing it through a utilization circuit (consists of full wave bridge rectifier and 4-pulses inverter controlled by PWM circuit) in order to rectify the distorted current I_h into sinusoidal current called I_u and feed different loads.

Three cases have been assessed and compared using Matlab-simulink program. It can be concluded from the results of the simulation, that the utilized current I_u can be created by rectifying harmonics current I_h with a simple cost and without requiring any additional source current I_s and a simple RL-load can be fed by I_u .

REFERENCES

- [1] C. Francisco, *Harmonics and power systems*. CRC press, 2006.
- [2] B. Wu, S. Rizzo, N. Zargari, and Y. Xiao, "An integrated dc link choke for elimination of motor common-mode voltage in medium voltage drives," in *Industry Applications Conference, 2001. Thirty-Sixth IAS Annual Meeting. Conference Record of the 2001 IEEE*, vol. 3, Sept 2001, pp. 2022–2027 vol.3.
- [3] J. Persson, "Comparing harmonics mitigation techniques," 2014.
- [4] J. Rodriguez, J. Pontt, R. Huerta, and P. Newman, "24-pulse active front end rectifier with low switching frequency," in *Power Electronics Specialists Conference, 2004. PESC 04. 2004 IEEE 35th Annual*, vol. 5. IEEE, 2004, pp. 3517–3523.
- [5] A. Nassif, W. Xu, and W. Freitas, "An investigation on the selection of filter topologies for passive filter applications," *Power Delivery, IEEE Transactions on*, vol. 24, no. 3, pp. 1710–1718, July 2009.
- [6] A. Massoud, K. Ahmed, S. Finney, and B. Williams, "Harmonic distortion-based island detection technique for inverter-based distributed generation," *Renewable Power Generation, IET*, vol. 3, no. 4, pp. 493–507, 2009.
- [7] S.-I. Jang and K.-H. Kim, "An islanding detection method for distributed generations using voltage unbalance and total harmonic distortion of current," *Power Delivery, IEEE Transactions on*, vol. 19, no. 2, pp. 745–752, 2004.
- [8] Z. Yuan, S. de Haan, and B. Ferreira, in *Power Electronics and Applications, 2007 European Conference on*, Sept, pp. 1–4.
- [9] Z. Yuan, S. de Haan, J. Ferreira, and D. Cvoric, "A facts device: Distributed power-flow controller (dpfc)," *Power Electronics, IEEE Transactions on*, vol. 25, no. 10, pp. 2564–2572, Oct 2010.
- [10] J. J. Toth and D. J. Velazquez, "Benefits of an automated on-line harmonic measurement system," *Industry Applications, IEEE Transactions on*, vol. IA-22, no. 5, pp. 952–963, Sept 1986.
- [11] A. Iqbal, E. Levi, M. Jones, and S. Vukosavic, "Generalised sinusoidal pwm with harmonic injection for multi-phase vsis," in *Power Electronics Specialists Conference, 2006. PESC'06. 37th IEEE*. IEEE, 2006, pp. 1–7.
- [12] J. Jose, G. Goyal, and M. Aware, "Improved inverter utilisation using third harmonic injection," in *Power Electronics, Drives and Energy Systems (PEDES) & 2010 Power India, 2010 Joint International Conference on*. IEEE, 2010, pp. 1–6.
- [13] J. Boys and S. Walton, "A loss minimised sinusoidal pwm inverter," in *IEE Proceedings B (Electric Power Applications)*, vol. 132, no. 5. IET, 1985, pp. 260–268.
- [14] H. Al-bayaty, M. Ambroze, and M. Z. Ahmed, "Taking advantage of the harmonics at the load side using passive filters," in *Systems and Informatics (ICSAI), 2014 2nd International Conference on*, Nov 2014, pp. 169–174.
- [15] —, "The benefit of harmonics current using a new topology of hybrid active power filter," in *2015 IEEE 5th International Conference on Power Engineering, Energy and Electrical Drives (POWERENG)*, May 2015, pp. 330–336.